

FIG. 1

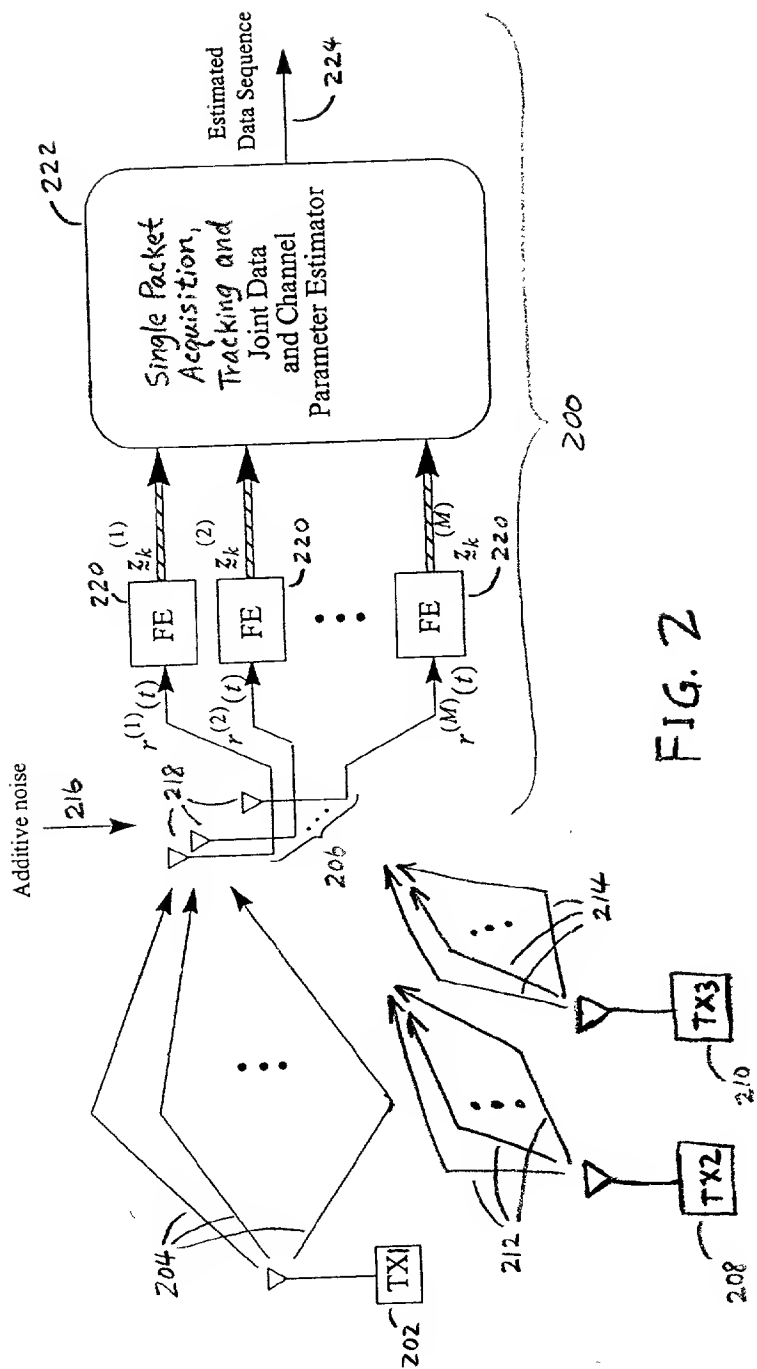


FIG. 2

222

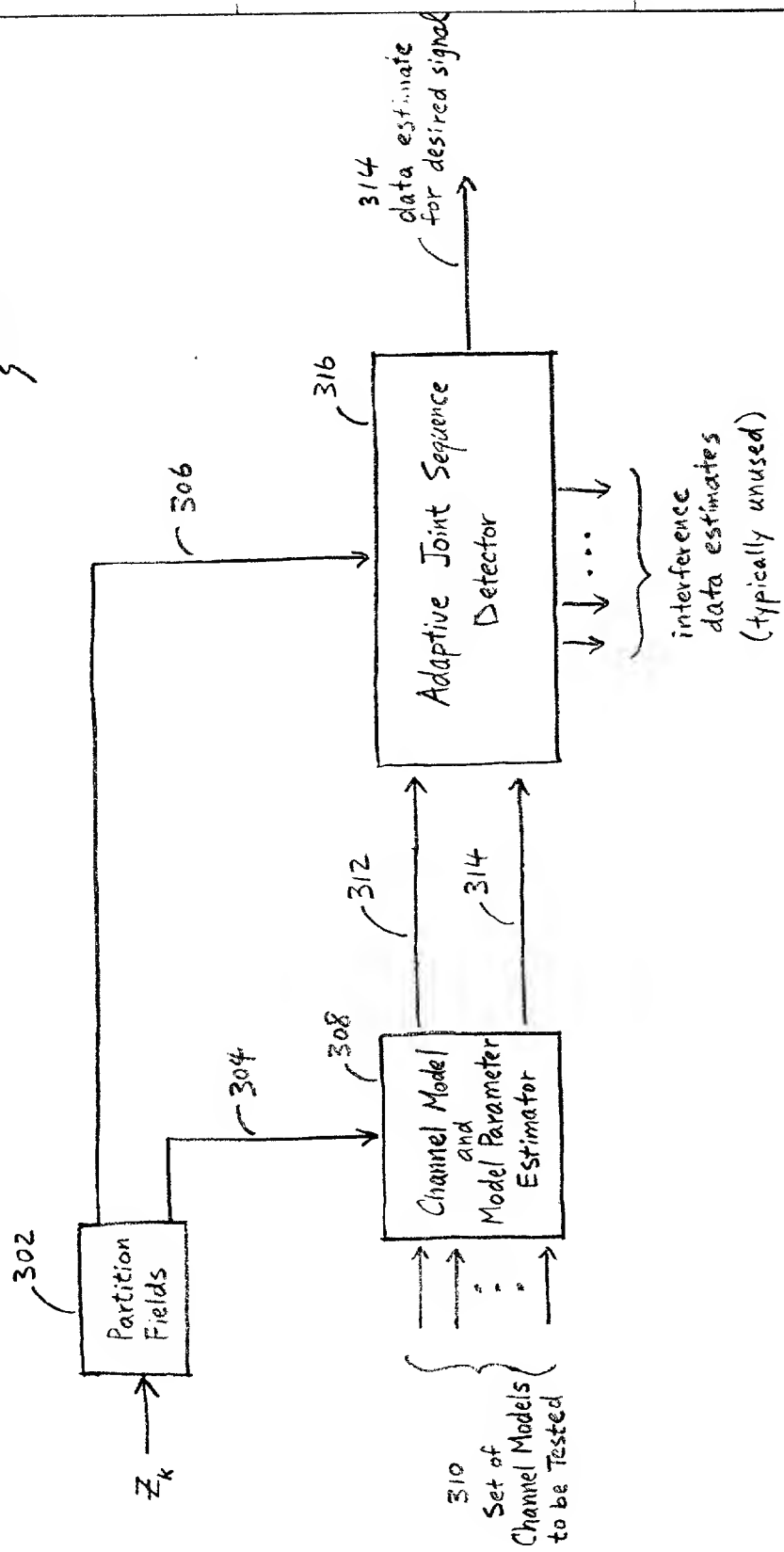


FIG. 3

FIG. 4A is a block diagram of a CA-MLSD receiver 400. The receiver 400 receives an input signal  $z_k$  (306) and outputs an Estimated Data Stream (412). The receiver 400 includes a Viterbi Processor (402) and a Parameter Estimator (406). The input signal  $z_k$  (306) is fed into the Viterbi Processor (402) and a delay block (408). The output of the delay block (408) is fed into the Parameter Estimator (406). The Parameter Estimator (406) outputs a d-step outdated parameter estimate (410) to the Viterbi Processor (402). The Viterbi Processor (402) outputs Tentative decisions (delay d) (404) to the Parameter Estimator (406). The Viterbi Processor (402) also outputs the Estimated Data Stream (412).

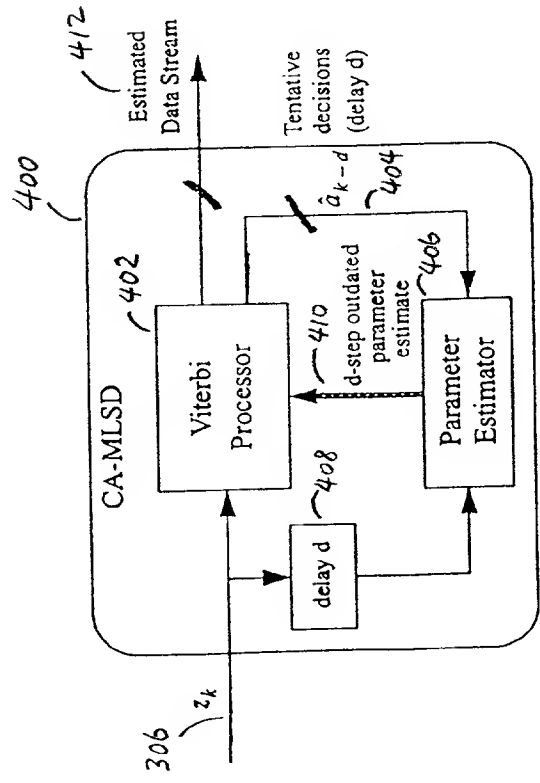


FIG. 4A

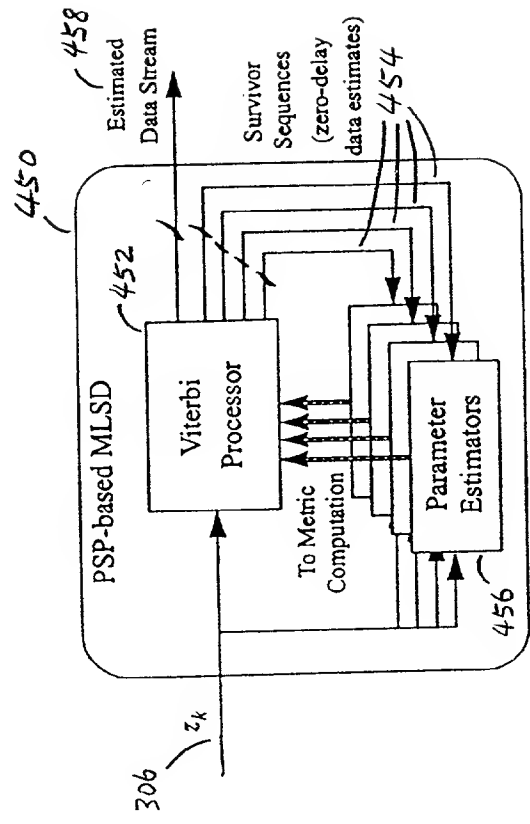


FIG. 4B

22-141 50 SHEETS  
22-142 100 SHEETS  
22-144 200 SHEETS



22-141 50 SHEETS  
22-142 100 SHEETS  
22-144 200 SHEETS

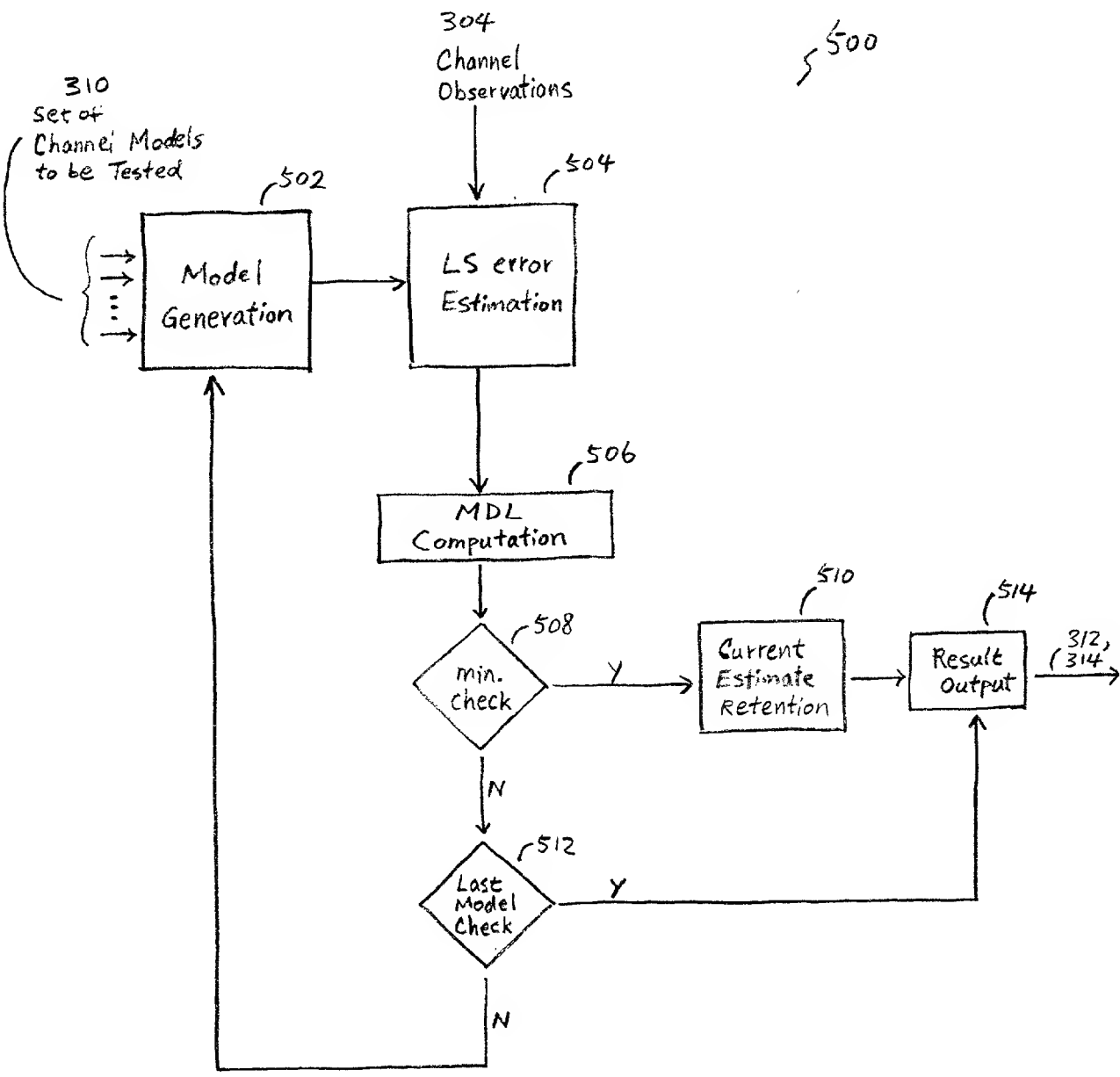


FIG. 5